

Notice of References Cited	Application/Control No. 10/604,056	Applicant(s)/Patent Under Reexamination WILLE ET AL.	
	Examiner Eric B. Chen	Art Unit 1765	Page 1 of 1

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*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
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	B	US-6,583,047	06-2003	Daniels et al.	438/638
	C	US-2004/0087164	05-2004	Bao et al.	438/694
	D	US-6,720,256	04-2004	Wu et al.	438/638
	E	US-6,835,652	12-2004	Fujimoto, Masashi	438/636
	F	US-2004/0018721	01-2004	Kim et al.	438/637
	G	US-6,365,529	04-2002	Hussein et al.	438/780
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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Wolf et al., Silicon Processing for the VLSI Era, 1986, Lattice Press, Vol. 1, pp. 182-83.
	V	Wolf, Silicon Processing for the VLSI Era, 2002, Lattice Press, Vol. 4, pp. 689-90.
	W	
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
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